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ATTORNEY DOCKET NO. CONFIRMATION NO. APPLICATION NO. FIRST NAMED INVENTOR FILING DATE 38979-11CPA2 2400 Petro Estakhri 09/620,544 07/21/2000 EXAMINER 7590 BRAGDON, REGINALD GLENWOOD LAW OFFICES OF IMAM 111 N. MARKET STREET, SUITE 1010 ART UNIT PAPER NUMBER SAN JOSE, CA 95113 2188

DATE MAILED: 09/13/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

### Applicant(s) Application No. ESTAKHRI ET AL. 09/620,544 Advisory Action Examiner Art Unit Reginald G. Bragdon 2188 --The MAILING DATE of this communication appears on the cover sheet with the correspondence address --THE REPLY FILED 30 August 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. PERIOD FOR REPLY [check either a) or b)] The period for reply expires \_\_\_\_\_months from the mailing date of the final rejection. b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). 1. A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal. 2. The proposed amendment(s) will not be entered because: (a) \( \times\) they raise new issues that would require further consideration and/or search (see NOTE below); (b) they raise the issue of new matter (see Note below); (c) they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or (d) \( \subseteq \) they present additional claims without canceling a corresponding number of finally rejected claims. NOTE: The amendments to the claims would require further search and/or consideration. 3. Applicant's reply has overcome the following rejection(s): 4. Newly proposed or amended claim(s) \_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s). 5. The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: 6. The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection. 7. ☑ For purposes of Appeal, the proposed amendment(s) a) ☑ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended. The status of the claim(s) is (or will be) as follows: Claim(s) allowed: \_\_\_\_\_. Claim(s) objected to: \_\_\_\_\_. Claim(s) rejected: 2-17. Claim(s) withdrawn from consideration: \_\_\_\_\_ 8. $\boxtimes$ The drawing correction filed on <u>30 August</u> 2004 is a) $\boxtimes$ approved or b) $\square$ disapproved by the Examiner.

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9. Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s).

Reginald G. Bragdon Primary Examiner Art Unit: 2188

Kegmald D. Bragdon

10. ☐ Other: See Continuation Sheet

Continuation of 10. Other: The IDS submitted 30 August 2004 has not been considered since Applicant has not met the requirements o 37 CFR 1.97(d). Note that Applicant may file a new information disclosure statement or correct the deficiency in the previously filed IDS (the IDS filed 21 June 2004), but the date that the new IDS or correction is filed will be the date of the IDS for purposes of determining compliance with the requirements based on the time of filing of the IDS (37 CFR 1.97)..

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Substitute for form 1449A/PTO STATEMENT BY APPLICANT

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С	omplete if Known
Application Number	09/620,544
Filing Date	07/21/2000
First Named Inventor	Petro Estakhri
Group Art Unit	2188
Examiner Name	Reginald G. Bragdon
Attorney Docket Number	38979-11C4

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Number

Filing Date

First Named
Inventor

Group Art Unit

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09/620,544

07/21/2000

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				Group Art Unit	2188
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Complete if Known Substitute for form 1449A/PTQ AUG 3 0 2004 **Application Number** 09/620,544 INFORMATION DISCLOSURE 07/21/2000 Filing Date STATEMENT BY APPLICANT **First Named Inventor** Petro Estakhri 2188 Group Art Unit (use as many sheets as necessary) Reginald G. Bragdon Examiner Name 38979-11C4 6 Attorney Docket Number of Sheet OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the anticle (when appropriate), title of the item (book, magazine, journal, serial, symposium, T2 Examiner catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published. MENDEL ROSENBLUM and JOHN K. OUSTERHOUT, The Design and Implementation of a Log-Structured File System, article, 1991, 15 pgs., C1 Berkeley, USA BRIAN DIPERT and MARKUS LEVY, Designing with Flash Memory, book, April 1994, 445 pgs., Annabooks, San Diego, USA C2 ROSS S. FINLAYSON and DAVID, R. CHERITON, An Extended File Service Exploiting Write-Once Storage, article, 1987, 10 pgs. ACM C4 JASON GAIT, The Optical File Cabinet: A Random-Access File System for Write-Once Optical Disks, article, June 1988, 12 pgs., Beaverton, Oregon C<sub>5</sub> C6 HENRY G. BAKER, Memory Management, book, 1995, 19 pgs., Springer-Verlag Berlin Heidelberg, Germany SAPE J. MULLENDER and ANDREW S. TANENBÄUM, A Distributed File Service Based on Optimistic Concurrency Control, article, 1985, 12 pgs., C7 ACM HIROSHI NAKAMURA, JUNICHI MIYAMOTO, KENICHI'IMAMIYA and YOSHIHISA IWATA, A Novel Sense Amplifier for Flexible Voltage Operation **C8** NAND Flash Memories, symposium, 1995, VLSI Circuits Digest of Technical Papers,, 2 pgs HIROSHI NAKAMURA, JUNICHI MIYAMOTO, KENICHI IMAMIYA, YOSHIHISA IWATA, YOSHIHISA SUGIURA and HIDEKO OODAIRA, A Novel Sensing Scheme with On-Chip Page Copy for Flexible Voltage NAND Flash Memories, article, June 1996, 9 pgs..., Vol. E79-C, No.6 C9 TAKAAKI NOZAKI, TOSHIAKI TANAKA, YOSHIRO KIJIYA, EITA KINOSHITA, TATSUO TSUCHIYA 8ND YUTAKA HAYASHI, A 1-Mb EEPROM wilh C10 MONOS Memory Cell for Semiconductor Disk Application, article, 1991, 5 pgs., Journal Of Solid -State Circuits, Vol., 26, No. 4. KAI HWANG and FAYE A. BRIGGS, Computer Architecture and Parallel Processing, book, 1984, McGraw-Hill, Inc., 2 pgs., US C11 WALTER LAHTI and DEAN McCARRON, State of the Art Magnetic VS. Optical Store Data in a Flash, article, 1990, 7 pgs., Vol. 15, No. 12, McGraw-C12 Hill, Inc., ÙS C13 RON WILSON, INTEGRATED CIRCUITS, 1-Mbit flash memories seek their role in system design, article, March 1, 1989, 2 pgs., No.6, Tulsa, OK S. MEHROURA, J.H. YUAN, R.A. CEMEA, W.Y. CHIEN, D.C. GUTEMAN, G. SAMACHISA, R.D. NOMAN, M. MOFIDI, W. LEE, Y. FONG, A. MIHNEA E. HANN, R.W. GREGOR, E.P. EBERHARDT, J.R. RADOSEVICH, K.R. STILES, R.A. KOHLER, C.W. LEUNG, and T.J. MULROONEY, Serial 9Mb F C14 EEPROM for Solid State Disk Applications, symposium, 1992, 2 pgs., Mountain View, CA C15 STEVEN H. LEIBSON, Nonvolatile, in-circuit-reprogrammable memories, article, January 3, 1991, 12 pgs., EDN, Circle No.12 RAMON CACERES, FRED DOUGLIS, KAI LI and BRIAN MARSH, Operationg System Implications of Solid-State Mobile Computers, erticle, 7 pgs., C16 October 1993, Workshop on Workstation Operting Systems C17 MICHAEL WU and WILY ZWAENEPOEL, A Non-Votatile, Main Memory Storage System, 12 pgs., 1994, ACM, San Jose, CA USA DAVE BURSKY, Innovative flash memories match DRAM densities: available with a choice of features, flash memories are finding homes in many C18 systems (includes related articles on the origins of flash, and on the differences between NAND and NOR flash memories), article, May 16, 1994, 9 pgs., Electronic Design, v.42, n.10, The Gale Group ANTHONY CATALDO, New flash enhancements up ante, (Intel's 28F400BV-120 and 28F004BV-120, Atmel's AT29BV010 and AT29BV020, and Samsung Semiconductor's KM29V32000 "flash" memory" devices)(Product Announcement), article, March 13, 1995, 4 pgs., Electronic News, v.41, C19 n.2056, The Gale Group

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